

12

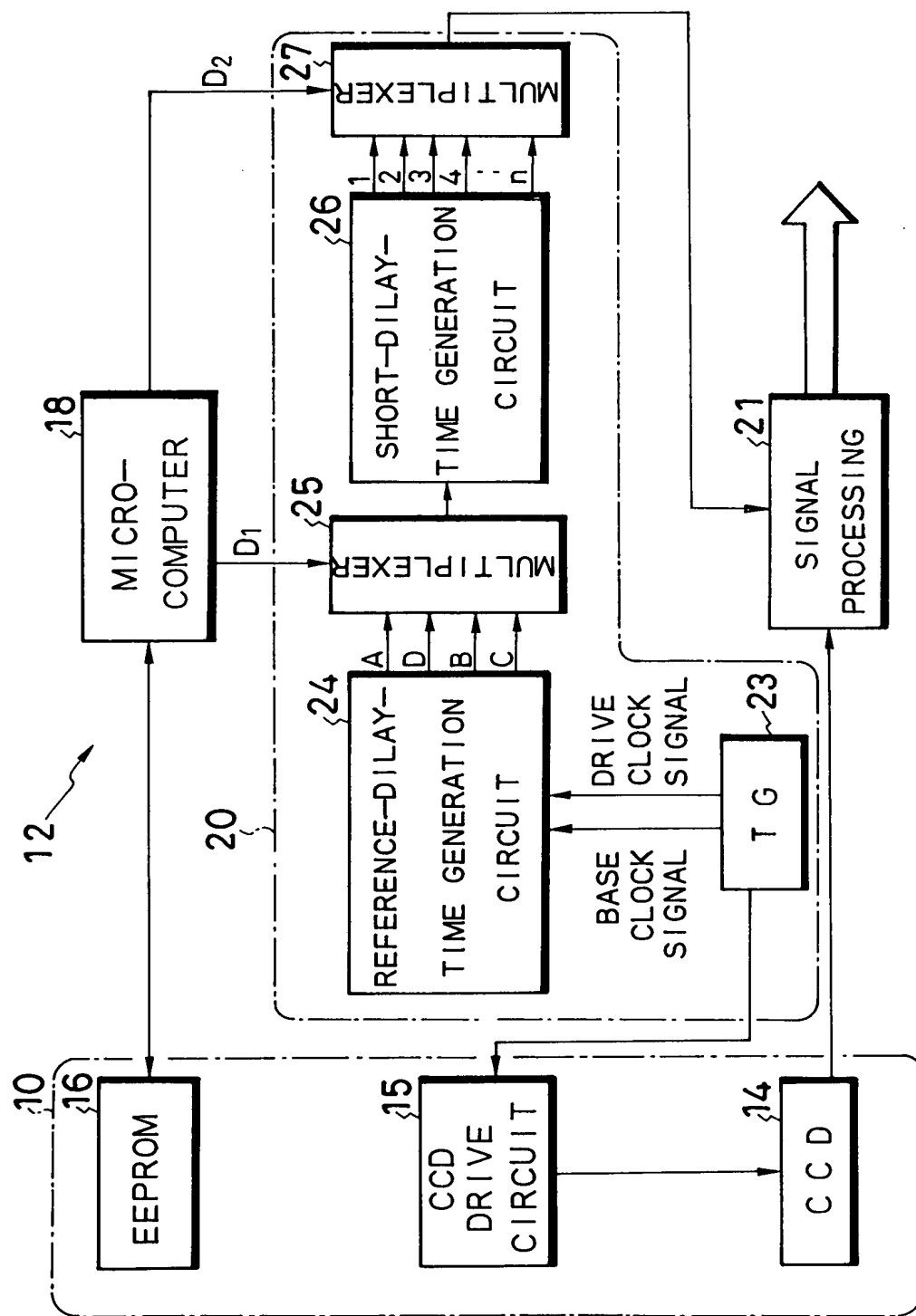


FIG. 2

DILAY-TIME-CONTROL
BIT DATA D₁

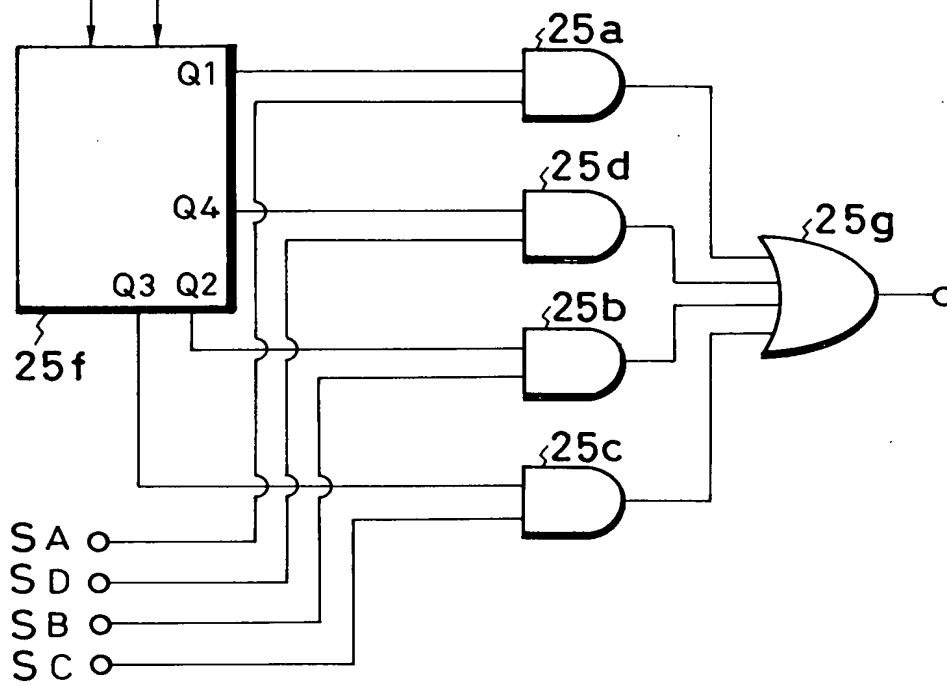


FIG. 3

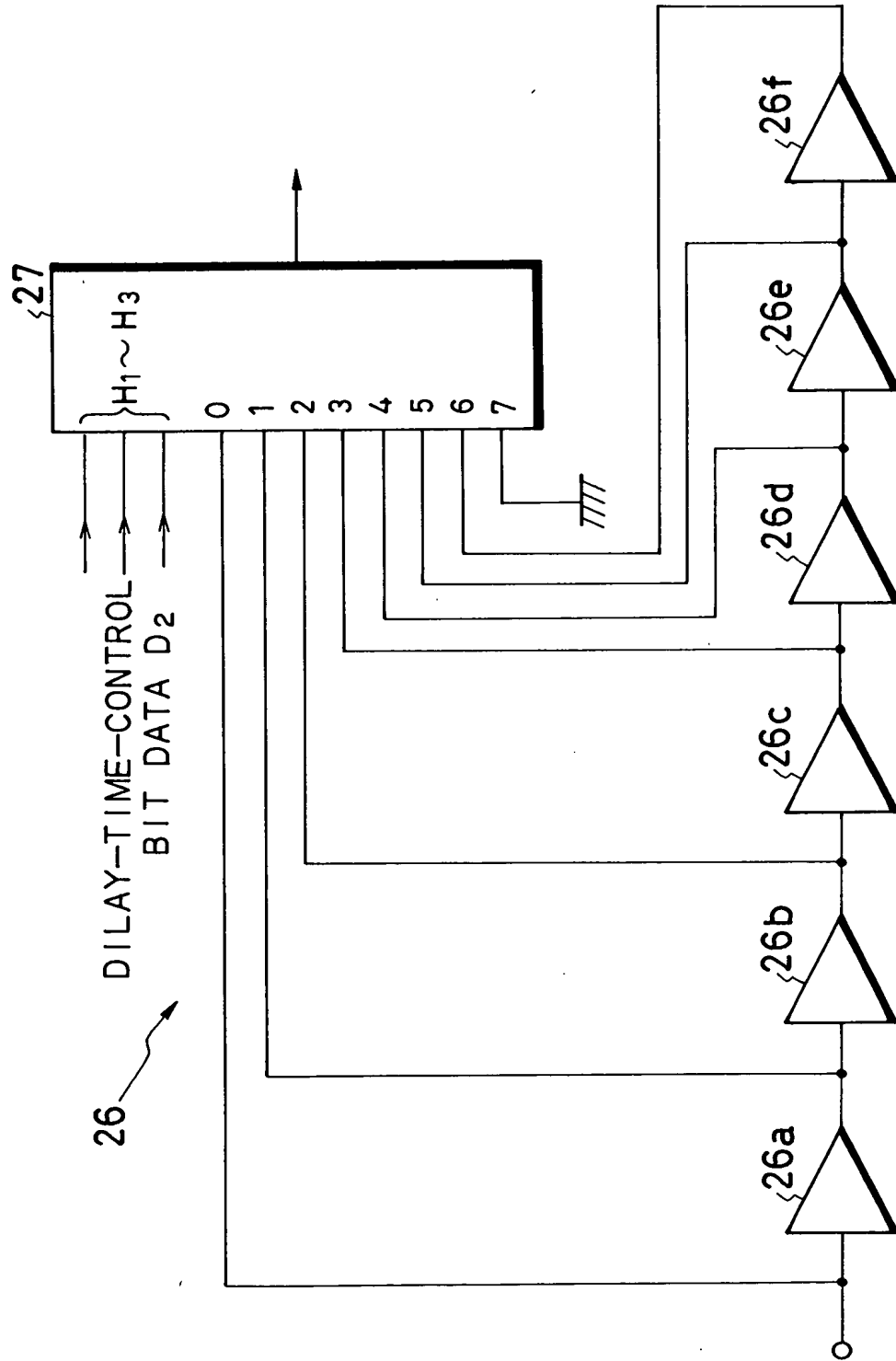


FIG. 4

